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H. T. Mouftah
May 1976 **Proceedings of the sixth international symposium on Multiple-valued logic**

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A system of three-valued algebra, based on operators previously defined, is presented. COS/MOS integrated circuits are applied to realize the three-valued operators. An approach to avoid the use of diodes in COS/MOS integrated circuit technology is given and a method for reduction of logical circuitry is discussed.

2 Burroughs Truth Function Evaluator
William Miehle
April 1957 **Journal of the ACM (JACM)**, Volume 4 Issue 2

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